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FIG. 1

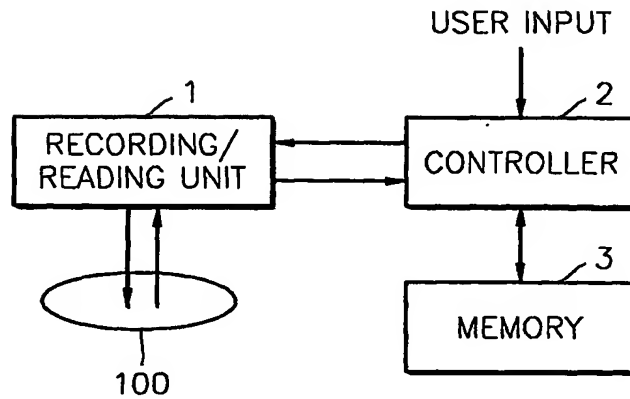


FIG. 2A

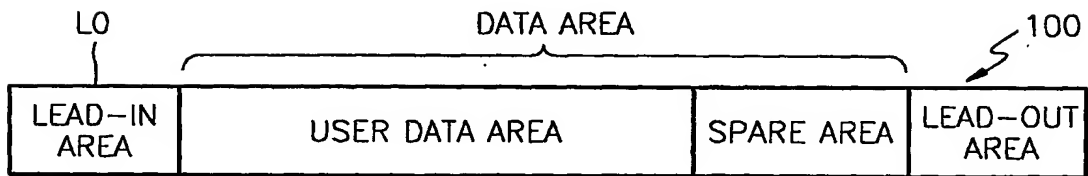
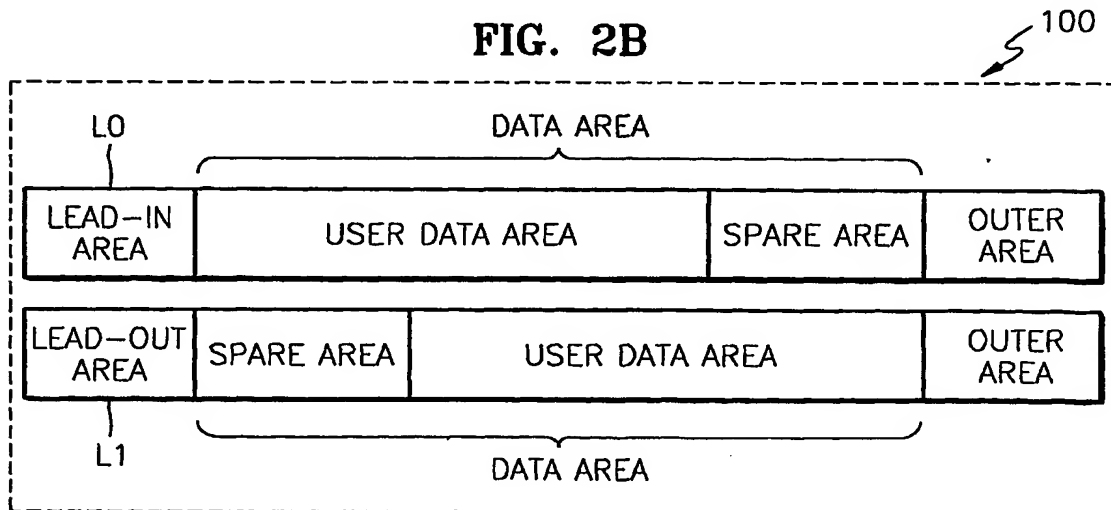
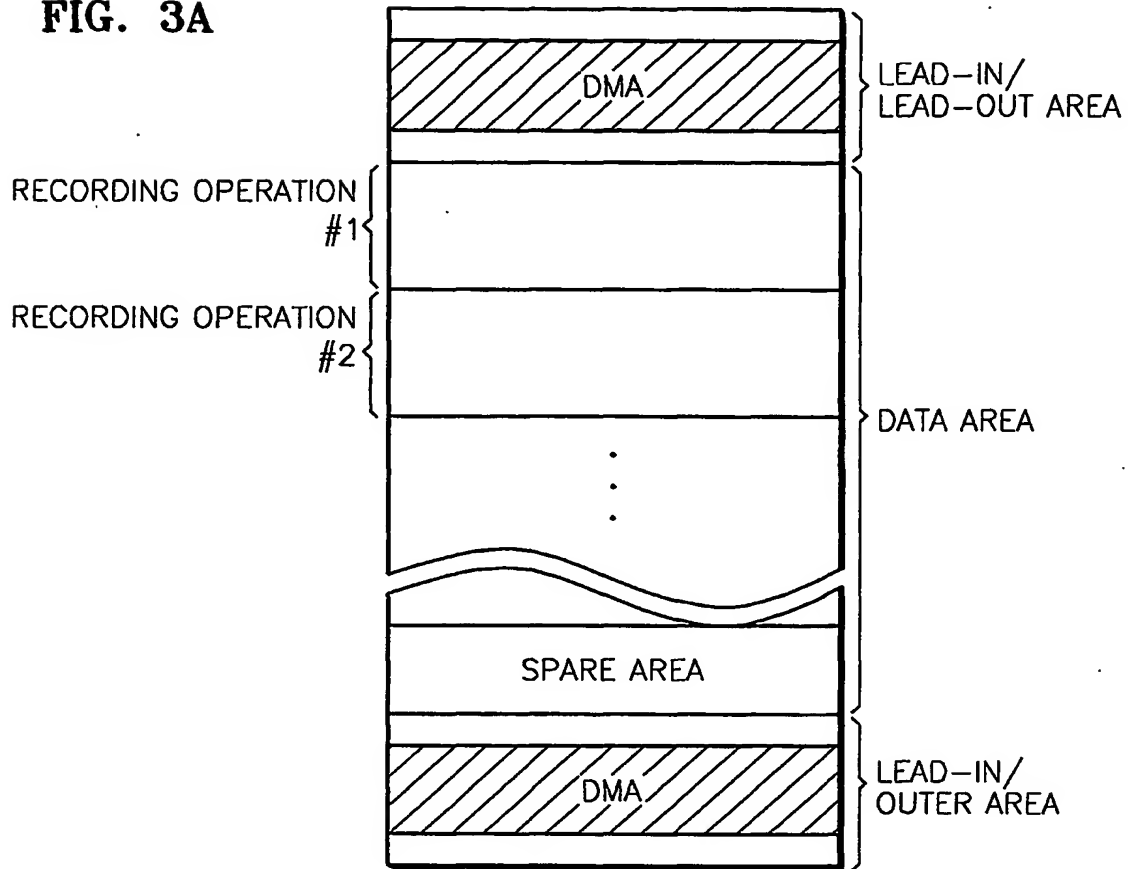


FIG. 2B



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**FIG. 3A**

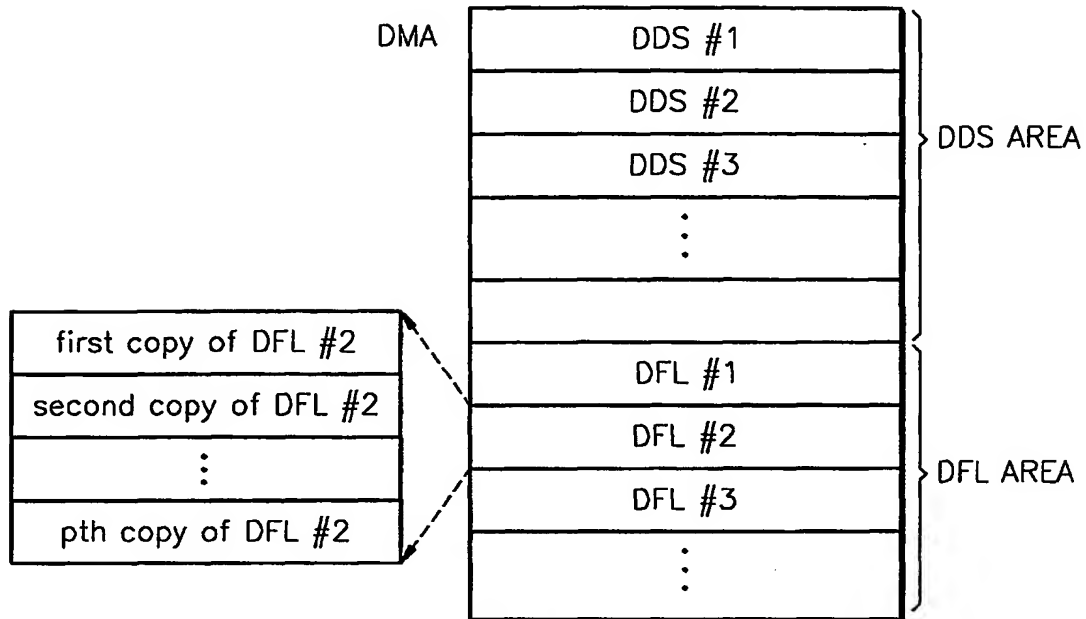


**FIG. 3B**

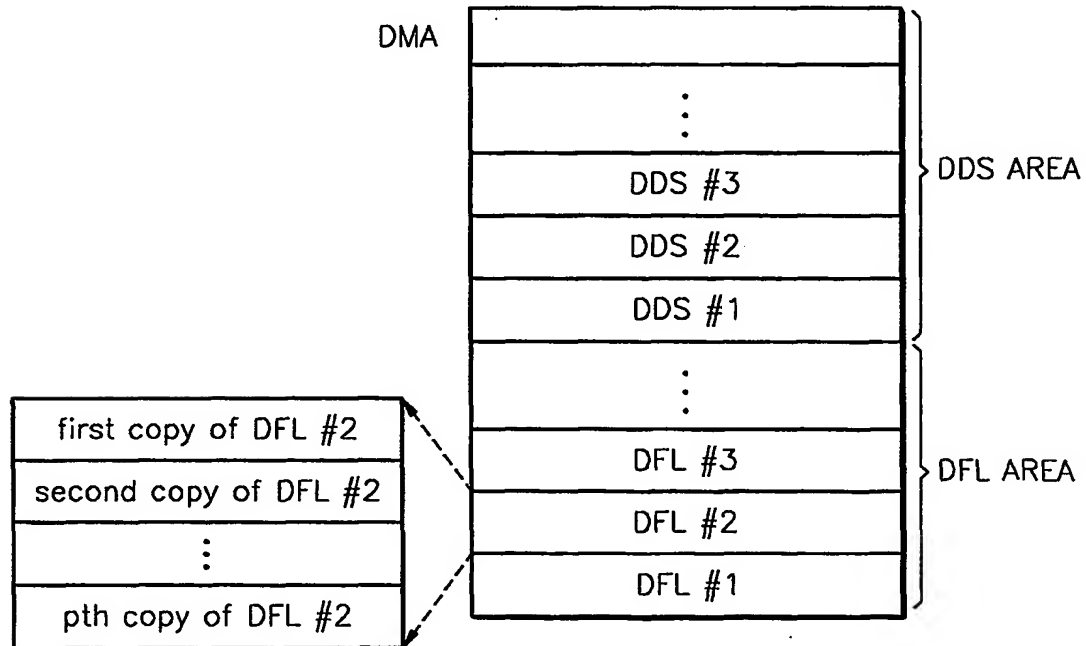
DMA 2
Buffer 3
Test
Buffer 2
DMA 1
Drive and Disc information
Buffer 1

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**FIG. 4A**

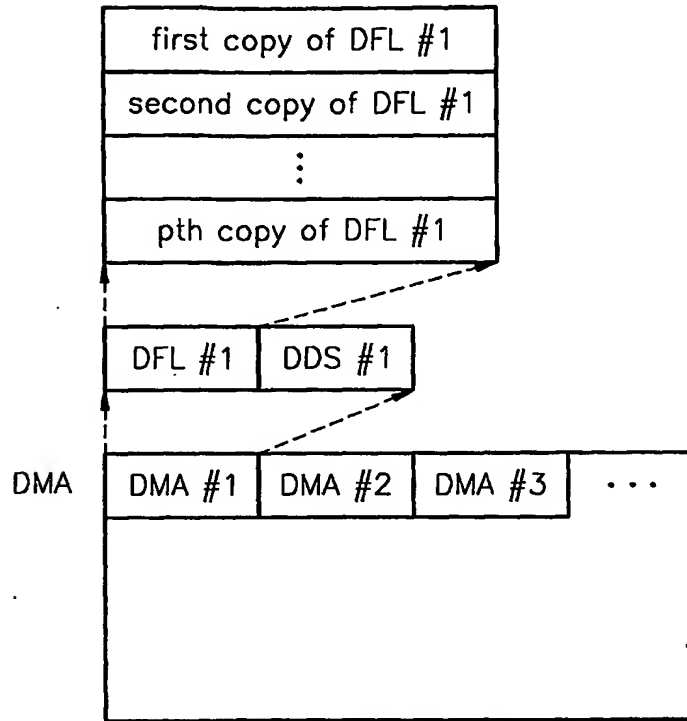


**FIG. 4B**



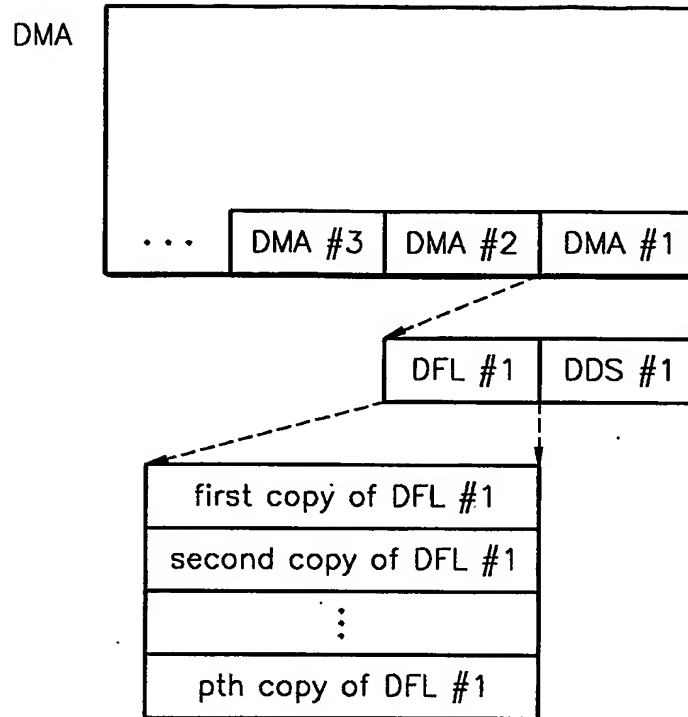
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**FIG. 4C**



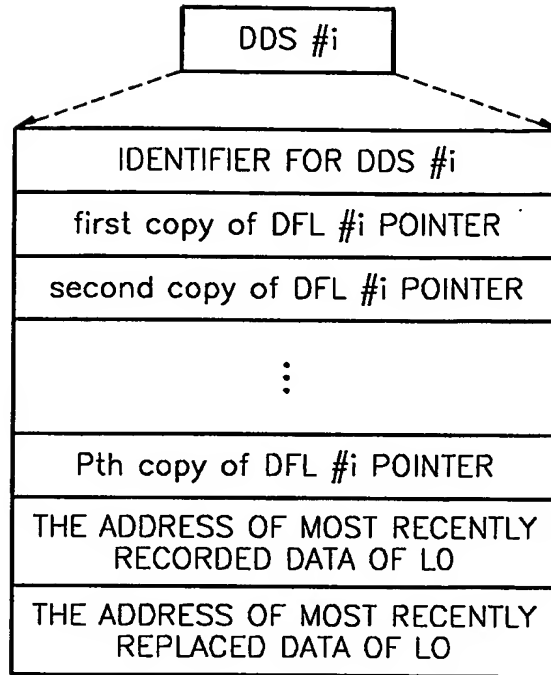
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**FIG. 4D**



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**FIG. 5A**



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FIG. 5B

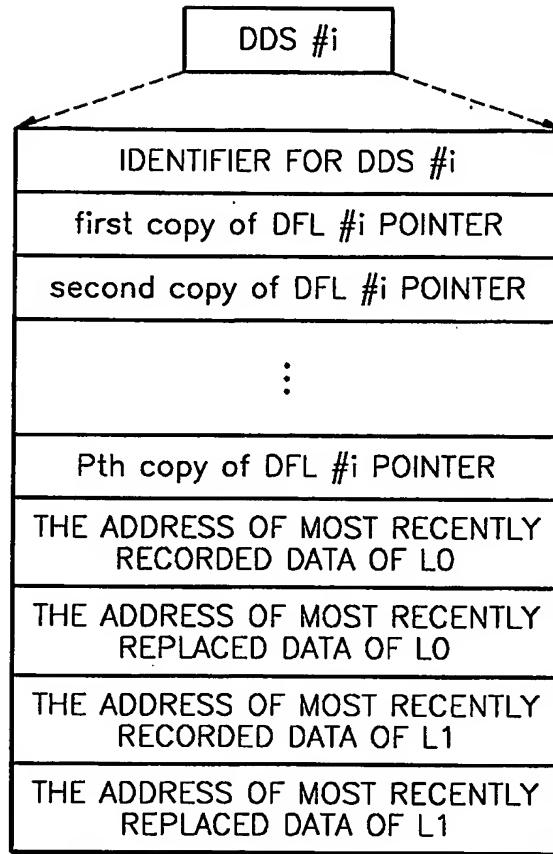
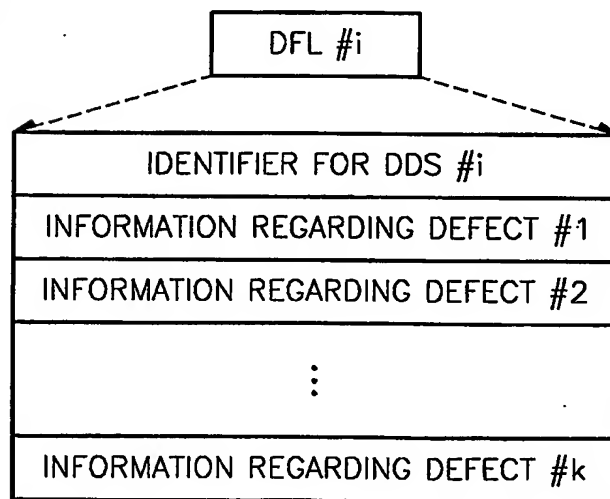
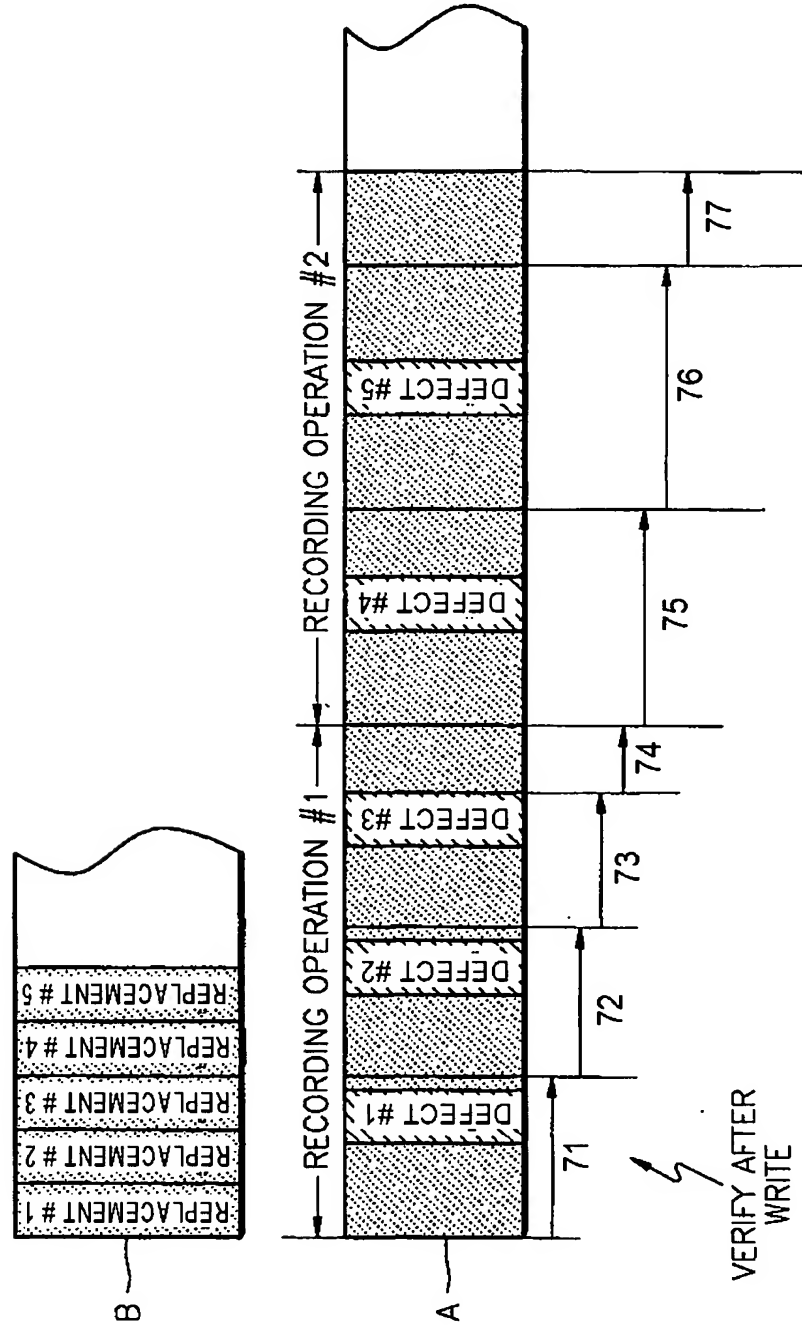


FIG. 6



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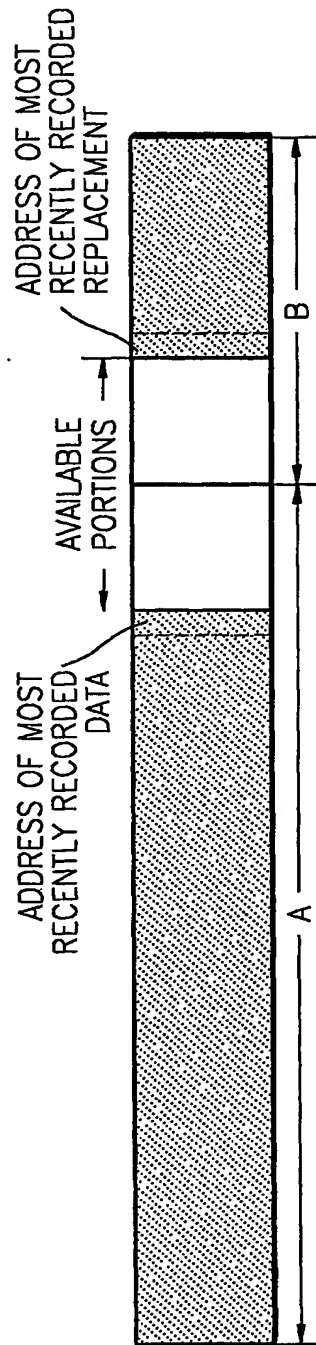
FIG. 7





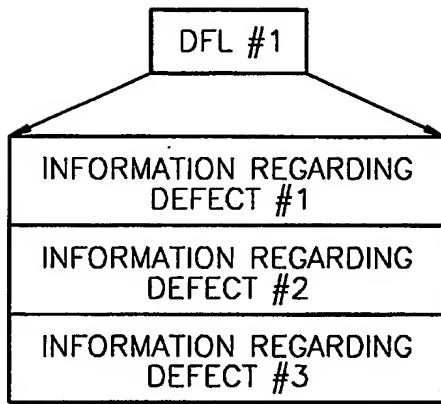
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FIG. 8

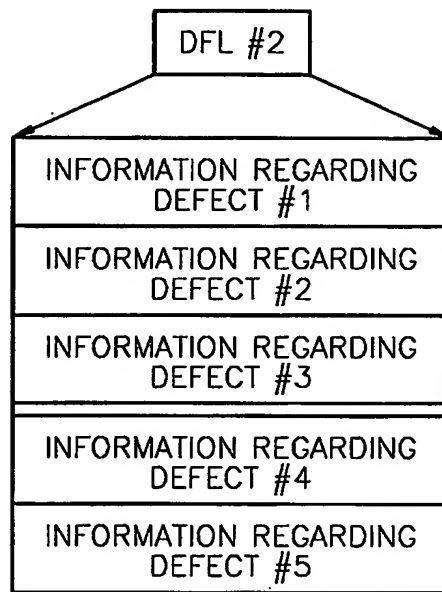


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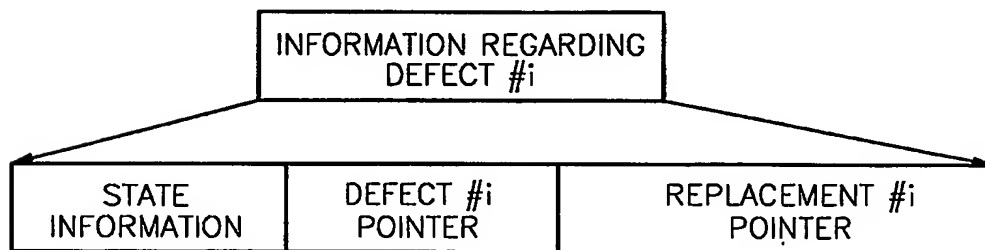
**FIG. 9A**



**FIG. 9B**

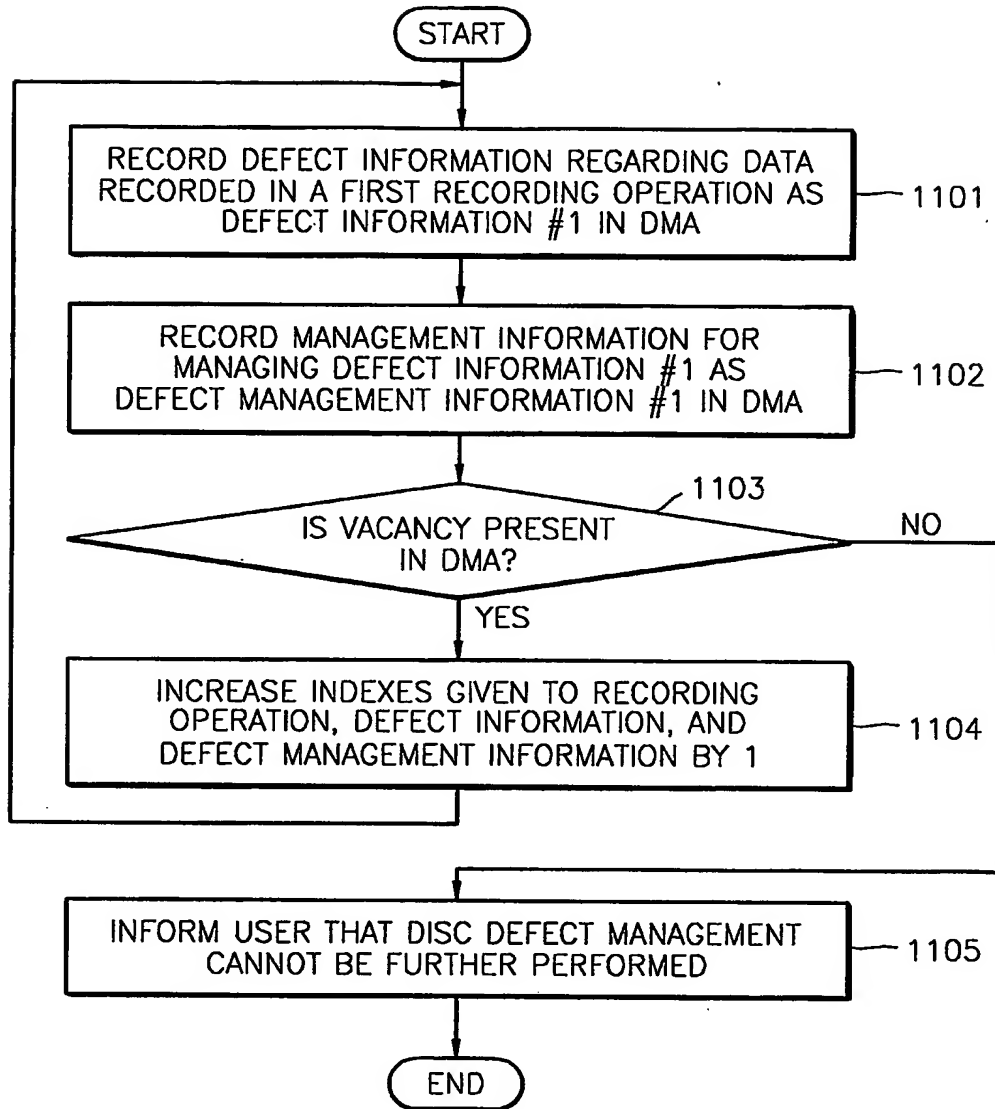


**FIG. 10**



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FIG. 11



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FIG. 12

